

UNIVERSITY OF KERALA

TIMETABLE FOR THE FOURTH SEMESTER B.TECH DEGREE EXAMINATION

JULY/AUGUST 2016

2008 SCHEME

(SUPPLEMENTARY CANDIDATES, PART-TIME CANDIDATES

TRANSITORY CANDIDATES AND MERCY CHANCE CANDIDATES UP TO 2004 ADMISSION)

DAY	DATE	TIME	SUBJECT
WEDNESDAY	27/07/2016	1.30 pm to 4.30pm	Engineering Mathematics III (CMNPHERUFB) Engineering Mathematics III(S) Engineering Mathematics III – Probability and Random Processes (TA)
FRIDAY	29/07/2016	2.00pm to 5.00pm	Humanities (CHTARFB) Computer Programming and Numerical Methods (MNPU) Aircraft Structure I (S)
MONDAY	01/08/2016	1.30pm to 5.30pm	Machine Drawing (M) Production Drawing (P)
		1.30pm to 4.30pm	Building Planning & Drawing (C) Introduction to Industrial Engineering (N) Heat Transfer Operations -I (H) Chemical and Biochemical Reaction Engineering (B) Digital Electronics & Logic Design(E) Analog Communication (T) Basic Instrumentation (A) Introduction to Aerospace Engineering(S)
WEDNESDAY	03/08/2016	1.30pm to 4.30pm	Fluid Mechanics II (C) Manufacturing Process (MN) Manufacturing Technology (U) Fluid Flow Operations I (H) Computer Programming in C++ (B) Electrical Measurements -I (E) Object Oriented Techniques (RF) Production Technology (S)

FRIDAY	05/08/2016	2.00pm to 5.00pm	Surveying II (C) Thermal Engineering (MU) Operations Management (N) Machine Tools-I (P) Industrial Bioprocess Technology (B) Engineering Materials Science(E) Analog Integrated Circuits (T) Digital Circuit Design (A) Data Structures and Algorithms(RF) Aerodynamics(S)
MONDAY	08/08/2016	1.30pmt04.30pm	Structural Analysis-I (C) Metallurgy & Material Science (MP) Fluid Mechanics & Hydraulic Machines(N) Auto Power Plant (U) Microcontroller- based Design(F) Organic Chemistry (H) Molecular Biology (B) Engineering Electro-magnetics (E) Signals & Systems (TA) Computer Hardware Design (R) Material Science and Metallurgy(S)
WEDNESDAY	10/08/2016	1.30pm to 4.30pm	Operating Systems(R) Automobile Engineering Drawing (U) Database Design (F) Electronics Circuits II (T) Power System Engineering-I (E)
THURSDAY	12/08/2016	1:30pm to 4:30pm	Linear Integrated Circuits (A) Production Process – I (P) Electrical Technology (H)

University Buildings,
Thiruvananthapuram

CONTROLLER OF EXAMINATIONS